



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
NATIONAL INSTITUTE OF TECHNOLOGY PUDUCHERRY
(An Institute of National Importance under MHRD, Govt. of India)
THIRUVETTAKUDY, KARAIKAL – 609 609

Advertisement No. : NITPY / ECE / SMDP-C2SD PROJECT / 2016 / 01

08/08/2016

Advertisement for recruitment of Lab Engineer (LE) and Project Assistant (PA)

WALK IN INTERVIEW

Applications are invited from eligible candidates for the position of **Lab Engineer** and **Project Assistant (PA)** under the Special Manpower Development Programme for Chip to Systems Development (**SMDP-C2SD**) project carried out by Department of ECE. Candidates should appear for the written test/interview with original and attested copies of all academic and experience certificates, testimonials, completed application, bio data and pass port size photo on **20th Aug 2016** at **08:30 AM** in the Department of ECE, NIT Puducherry.

Funding Agency of the Project: DeitY/MCIT, New Delhi

Project Position and Number: Project Assistant -1, Lab Engineer-1

Salary and Tenure:

S. No.	Post	Desired qualification	Responsibilities	Consolidated salary (per month)
1.	Lab Engineer	The minimum qualification is I Class M.E./ M.Tech in Signal Processing / VLSI Design and I Class B.E/ B.Tech in Electronics and Communication Engineering with sound working knowledge on VLSI CAD Tools, UNIX / MATLAB / scripting and relevant areas.	<ol style="list-style-type: none">1. Maintenance and development of lab equipment2. Carrying out R&D work3. Installation of VLSI tools and use them for design4. Assisting students during the lab classes on speech processing, VLSI and DSP5. Undergoing training in advanced topics on signal processing and VLSI6. Preparation of project reports for submitting to project monitoring and project funding agency7. Any other administration duties assigned by CI or Co-CI	Upto Rs. 34000 /-
2.	Project Assistant	(B.E/B.Tech) in Electronics or Electronics and	<ol style="list-style-type: none">1. Literature survey2. Data collection and	Upto Rs 20000/-

		communication or computer science or Electrical and Electronics Engineering with minimum CGPA of 6.5 / (ME/M.Tech) in the fields of Embedded /VLSI. Knowledge in VLSI CAD tools, Matlab, open CV, C++, UNIX and relevant areas	preparation 3. Analysis of collected data 4. Maintenance of lab equipment 5. Carrying out R&D work 6. Preparation of project reports for submitting to project monitoring and project funding agency 7. Developing the skills on advanced signal/speech processing and VLSI 8. Any other administration duties assigned by CI or Co-CI	
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The posts are purely temporary in nature. The appointment may initially be made for a period of 1 year and the tenure may be extended for an additional 1 year depending upon the performance.

Registration for Ph.D.: Temporary staffs, if found suitable, can register for Ph.D as per Institute noms.

Note: No TA/DA will be paid for attending the interview.

Candidates may also send their soft copies of RESUME in advance to Dr. G Lakshmi Sutha, Chief Investigator, SMDP-C2SD project, Department of ECE, NIT Puducherry, Karaikal. (E-mail: lakshmi.sutha1@gmail.com)

IMPORTANT

Reporting and Registration Time: **8.30 AM to 9.00 AM**

Date: **20-08-2016**

Venue: **NIT PUDUCHERRY, Thiruvettakudy, Karaikal, Puducherry 609609.**